

IN THE CLAIMS:

Please amend and re-number claims 9-20 and add claims 20-25 as follows.

- 1 1. (Original): A liquid crystal display device comprising:
2 a pair of substrates,
3 a liquid crystal layer interposed between said pair of substrates,
4 a wiring having a stacked structure layer formed on one of said pair of
5 substrates,
6 a transparent conductive film formed over said wiring,
7 said wiring includes a first layer of aluminum or an alloy comprising
8 essentially of aluminum, and at least a second layer of material selected from the group
9 including of molybdenum, aluminum, chromium, tungsten, silver, and copper.
- 1 2. (Original): The liquid crystal display device according to claim 1
2 wherein said second layer is formed on said first layer.
- 1 3. (Original): The liquid crystal display device according to claim 1
2 wherein said transparent conductive film includes at least one of: ITO, IZO and IGO.
- 1 4. (Original): The liquid crystal display device according to claim 1
2 further including a plurality of pixel parts being constructed with a plurality of gate lines
3 and a plurality of drain lines arranged in a matrix on one of said pair of substrates, and a
4 switching element being provided in each of said pixel parts,
5 wherein one of said plurality of drain lines comprises said wiring.
- 1 5. (Original): The liquid crystal display device according to claim 1
2 further including a plurality of pixel parts being constructed with a plurality of gate lines
3 and a plurality of drain lines arranged in a matrix on one of said pair of substrates, and a
4 switching element being provided in each of said pixel parts,
5 wherein one of said plurality of gate lines comprises said wiring .

1 6. (Original): The liquid crystal display device according to claim 5
2 wherein said plurality of gate lines are formed along a first direction in one of said pair of
3 substrates, said plurality of drain lines formed along a second direction in one of said pair
4 of substrates, a plurality of counter voltage signal lines formed along the first direction in
5 one of said pair of substrates,
6 wherein said one of plurality of counter voltage signal lines comprises said
7 wiring.

1 7. (Original): The liquid crystal display device according to claim 6
2 further including a counter electrode disposed in said pixel part and connected with said
3 one of plurality of counter voltage signal lines, said counter electrode having a rectilinear
4 shape or a comb shape.

1 98. (Re-numbered): The liquid crystal display device according to
2 claim 7 further including a comb-shape pixel electrode disposed in said pixel part and
3 connected with said switching element.

1 109. (Re-numbered and Currently amended): The liquid crystal display
2 device according to claim 9-8 further including an insulation layer, wherein said counter
3 electrode is formed on one of said pair of ~~electrodes~~substrates, said insulating layer is
4 formed over said counter electrode, said pixel electrode is formed on said insulating
5 layer.

1 110. (Re-numbered and Currently amended): The liquid crystal display
2 device according to claim 9-8 further including a scan signal applied through one of said
3 plurality of gate lines to said switching element, a video signal is applied through one of
4 said plurality of drain lines and said switching element to said pixel electrode, said
5 switching element formed proximate to a crossing point between said one of said of drain
6 lines and said one of said gate lines.

1 ~~42~~11. (Re-numbered and Currently amended): The liquid crystal display
2 device according to claim ~~9~~8 wherein said pixel electrode has a zigzag-shaped structure.

1 ~~43~~12. (Re-numbered and Currently amended): The liquid crystal display
2 device according to claim ~~9~~8 wherein said pixel electrode has a comb-shaped structure.

1 ~~44~~13. (Re-numbered and Currently amended): The liquid crystal display
2 device according to claim ~~43~~12 further including an insulation layer and an organic
3 layer, wherein said counter electrode is formed on one of said pair of
4 ~~electrodes~~substrates, said insulating layer is formed over said counter electrode, said
5 organic layer is formed over said insulating layer, said pixel electrode is formed on said
6 organic layer.

1 ~~45~~14. (Re-numbered): A liquid crystal display device comprising:
2 a pair of substrates,
3 a liquid crystal layer interposed between said pair of substrates,
4 drain lines and gate lines formed on one of said pair of substrates and
5 crossing each other in a matrix form,
6 counter voltage lines formed on one of said pair of substrates and being
7 disposed between said gate lines,
8 wherein at least one of said drain lines, said gate lines and said counter
9 voltage lines includes a multi-layered structure covered with a transparent conductive
10 film, said multi-layered structure comprising an aluminum layer or an alloy layer
11 comprising essentially of aluminum and a high-melting point metal layer, said transparent
12 conductive film including one of ITO, IZO and IGO.

1 ~~46~~15. (Re-numbered and Currently amended): The liquid crystal display
2 device according to claim ~~45~~14 further including a pixel electrode formed on one of said
3 pair of substrates and having a comb-shaped structure, and a switching element formed

4 proximate to a crossing point between said at least one of said drain lines and said gate
5 lines and connected with said pixel electrode.

1 ~~47~~16. (Re-numbered and Currently amended): The liquid crystal display
2 device according to claim ~~46~~15 further including a sheet of counter electrode disposed
3 on one of said pair of substrates in opposed relation to said pixel electrode and connected
4 with one of said counter voltage lines.

1 ~~48~~17. (Re-numbered and Currently amended): The liquid crystal display
2 device according to claim ~~46~~15 further including a comb-shaped counter electrode
3 disposed on one of said pair of substrates in opposed relation to said pixel electrode and
4 connected with one of said counter voltage lines.

1 ~~49~~18. (Re-numbered and Currently amended): A liquid crystal display
2 device comprising:
3 a pair of substrates,
4 a liquid crystal layer interposed therebetween,
5 a thin film transistor having a gate electrode, a source electrode and a
6 drain electrode formed on one of said pair of substrates;
7 a gate line connected to said gate electrode,
8 a drain line connected to said drain electrode,
9 a pixel electrode connected to said source electrode and having an
10 approximately a slit shape structure, a comb-shaped structure, or a zigzag-shaped
11 structure,
12 a counter electrode being any of ITO, IZO or IGO and arranged in
13 opposed relation to said pixel electrode,
14 a counter voltage line connected to said counter electrode,
15 wherein said counter voltage line comprising a triple-layered structure
16 including an alumina first layer, a high-melting point metal second layer, and a third layer
17 of aluminum or an alloy comprising essentially aluminum,

18 said high-melting point metal second layer connected to said counter
19 electrode through an opening in said alumina first layer.

1 2019. (Re-numbered and Currently amended): The liquid crystal display
2 device according to claim ~~19~~ 18 wherein said alumina first layer and said high-melting
3 point metal second layer are formed on said third layer, and
4 said high-melting point metal second layer formed through said alumina
5 layer from a surface side of a portion of said alumina layer to said third layer, and
6 connected to said counter electrode.

1 20. (New): a liquid crystal display device comprising:
2 a pair of substrates and a liquid crystal layer interposed therebetween;
3 a thin film transistor having a gate electrode, a source electrode, and a
4 drain electrode formed on one of said pair of substrates;
5 a gate line connected to said gate electrode;
6 a drain line connected to said drain electrode;
7 a pixel electrode connected to said source electrode and having an
8 approximately a slit-shaped structure, a comb-shaped structure, or a zigzag-shaped
9 structure;
10 a counter electrode comprising one ITO, IZO, and IGO, and arranged in
11 opposed relation to said pixel electrode;
12 a counter voltage line connected to said counter electrode,
13 wherein said counter voltage line comprises a triple-layered structure
14 including a molybdenum or a titanium first layer, an aluminum second layer, and a
15 molybdenum or titanium third layer.

1 21. (New): The liquid crystal display device according to claim 20
2 wherein said first layer is connected to said counter electrode.

1 22. (New): The liquid crystal display device according to claim 21
2 wherein a width of said first layer is greater than a width of said third layer.

1 23. (New): A liquid crystal display device comprising:
2 a pair of substrates and a liquid crystal layer interposed therebetween;
3 a thin film transistor having a gate electrode, a source electrode and a
4 drain electrode formed on one of said pair of substrates;
5 a gate line connected to said gate electrode;
6 a drain line connected to said drain electrode;
7 a pixel electrode connected to said source electrode;
8 a counter electrode being one of ITO, IZO, and IGO, and arranged in
9 opposed relation to said pixel electrode;
10 a counter voltage line connected to said counter electrode,
11 wherein said counter voltage line comprises a triple-layered structure
12 including a molybdenum or a titanium first layer, an aluminum second layer, and a
13 molybdenum or titanium third layer,
14 at least one of said pixel electrode and said counter electrode having an
15 approximately a slit-shaped structure, a comb-shaped structure, or a zigzag-shaped
16 structure

1 24. (New): The liquid crystal display device according to claim 23
2 wherein said first layer is connected to said counter electrode.

1 25. (New): The liquid crystal display device according to claim 24
2 wherein a width of said first layer is greater than a width of said third layer.